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(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Peter Poechmueller

Application No.: Not Yet Assigned

Group Art Unit: N/A

Filed: July 11, 2003

Examiner: Not Yet Assigned

For: STRUCTURE AND METHOD OF

MULTIPLEXING BITLINE SIGNALS

WITHIN A MEMORY ARRAY

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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Dear Sir:

It is respectfully requested that the references listed on the enclosed form be made of record and considered with respect to the above-referenced U.S. patent application. A copy of the reference is enclosed. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

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Dated: July 11, 2003

Respectfully submitted,

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l s	TATEMENT B	3Y /	APPLICANT	First Named Inventor	Peter Poechmueller	
				Art Unit	N/A	
	(use as many sh	eets as	s necessary)	Examiner Name	Not Yet Assigned	
Sheet	1	of	1	Attorney Docket Number	INTECH 3.0-094 03 P 50510 US	

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where	
		Number-Kind Code ² (if known)	MM-DD-YYYY		Relevant Passages or Relevant Figures Appear	
	AA	US 2002/0196651-A1	12-26-2002	Rolf Weis		

FOREIGN PATENT DOCUMENTS						
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